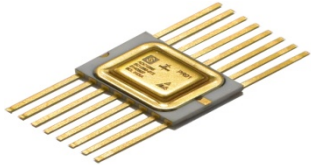
	8-Channel, 1 MSPS, 12-Bit Successive Approximation Analog to Digital Converter (ADC)	
SCL Part No.	SC1260-0T2	

PRODUCTDESCRIPTION:

The SC1260_0T2 device is a low-power, seven channel CMOS 12-bit analog-to-digital converter specified for conversion throughput rates of 50 kSPS to 1 MSPS. The converter is based on successive approximation register architecture with an internal track-and-hold circuit. The device can be configured to accept up to seven input signals at inputs IN0 through IN6. The output serial data is straight binary and is compatible with serial interfaces. The analog supply (V_A) can range from 3.0 V to 3.3V, and the digital supply (V_D) can range from 3.0 V to V_A . Normal power consumption using a 3.3V supply is 10 mW.

FEATURES:

- Operating Voltage: 3.3V
- Resolution: 12-Bit
- Data Rate: 1MSPS
- Input Range: 0V to 3.3V
- No missing code Guaranteed
- Output Data Format: Straight Binary, Serial output
- Power Consumption < 10mW
- SCL's 180nm CMOS Technology
- Operating Temperature (TA): -55°C to +125°C
- Packaged in 16 Pins

