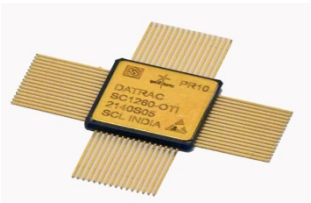
	10-Bit, 1.5 MSPS, Single Channel, Differential Ended Successive Approximation Analog to Digital Converter (ADC)	
SCL Part No.	**SC1260-0T1**	

PRODUCT DESCRIPTION:

The 10-bit SAR is a monolithic CMOS analog-to-digital converter capable of converting analog input signals into 12-bit digital word upto 1.5 Mega samples per second (MSPS). This converter uses a SAR architecture, operating on a single 3.3V power supply, device achieves 10-bits effective resolution and consumes <2mW power. The inputs provide a full scale input swing equal 0.65V to 2.65V. Full scale input range is recommended for optimum performance. The ASIC is fabricated in 0.18µm SCL CMOS Standard Logic Process.

FEATURES:

- Operating Voltage: 3.3V
- Resolution: 10-Bit
- Data Rate: 1.5MSPS
- Input Range: 4Vp-p Differential Ended
- No missing code Guaranteed
- Output Data Format: Straight Binary
- Power Consumption < 2mW
- SCL's 180nm CMOS Technology
- Operating Temperature (TA): -55ºC to +125ºC
- Packaged in 64 Pins, CQFP