

Semi-Conductor Laboratory Government of India

Ministry of Electronics & Information Technology, Sector-72, S.A.S. Nagar (Mohali)-160071, Near Chandigarh (Punjab)

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Tender Ref. No. SCL/P&S/44969/DTDD/2024

Addendum No. 1: to Tender Ref. No. SCL/P&S/ 44969/DTDD/2024 (Two-Part) for Development and Porting of Logic Multi-Time Programmable (MTP) Non-Volatile Memory (NVM) and Delivery of additional Memory Macros

The Bid Submission Due Date and time against our Tender has been extended till 04.06.2024 upto 15:00 hours (IST). All other terms and conditions remain unchanged.

प्रमुख, क्रय एवं भंडार /Head, Purchase & Stores

Dated: 06.05.2024