| | 8-Bit, 100 MSPS, Low Power Pipeline Analog to Digital Converter (ADC) | |
|---|---|---|
| **SCL Part No.** | **SC1261-0T1** | |
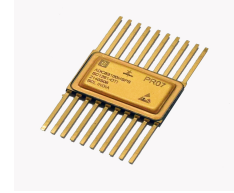
## PRODUCTDESCRIPTION:

The 8-bit 100 MSPS is a monolithic CMOS analog-to-digital converter capable of converting analog input signals into 8-bit digital word upto at 100 Mega samples per second (MSPS). This converter uses a differential, pipeline architecture with digital error correction. The differential inputs take a full scale differential swing up to 1Vpp. The device has on chip voltage reference.

FEATURES:

- Operating Voltage: 3.3V
- Resolution: 8-Bit
- Data Rate: upto 100MSPS
- Input Range: 1Vp-p Differential Ended/Single Ended
- No missing code Guaranteed
- Output Data Format: Straight Binary
- Power Consumption < 530mW
- SCL's 180nm CMOS Technology
- Operating Temperature (TA): -55ºC to +125ºC
- Packaged in 20 Pins CFP package