

32-Bit PROCESSOR KALPANA 3201 with LVR (MCM3)

KALPANA 3201 SC/M 200LVCMGA28P181 SC1132-0 2053S01 SCL VSSC

SCL Part No.

SC1132-0

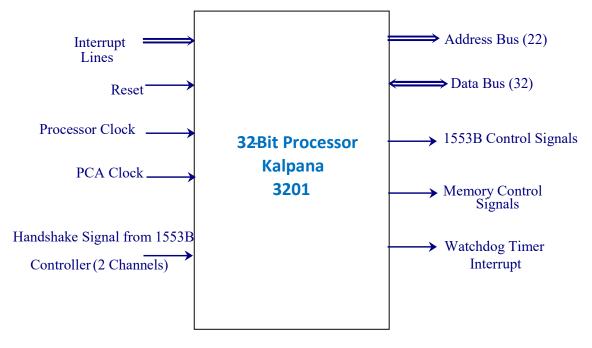
PRODUCT DESCRIPTION:

Kalpana 3201 is a 32-bit microprocessor with floating point support. Its architecture is based on IEEE-1754 ISA instruction set architecture. The processor employs RISC hardware control.

The device can operate over a large temperature range -55°C to +125°C and it is packaged in a hermetic sealed 181 pin grid array package. Data type Supported are 8/16/32/64-bit signed and unsigned Fixed Point and 32/64-bit IEE754 Floating Point.

FEATURES:

- 32-Bit Microprocessor
- Single 3.3V Supply Operation
- 210 MHz Operating Frequency
- Quiescent Supply Current <10 mA
- Operating Power <500mW
- Operating Temperature -55°C to 125°C
- Testability Features: Scan and Functional
- No of Instruction: 107, 32-bit wide
- External Addressing bus size: 22 bit
- Interrupts:11 (4 external,7 internal)
- Traps: 19 exceptions
- No. of Timers: 4 Nos. of 32-bit timers
- Two on-chip 1553B bus interfaces
- Packaged in 181 Pins, CPGA
- SCL's 180nm CMOS Technology



Functional Block Diagram