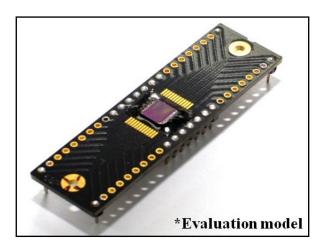
# RH-LDO Linear Voltage Regulator 1.2V/1.6A (SC1028-0)



## **DATASHEET**

**Version 1.0, June 2022** 



Semi-Conductor Laboratory Government of India S.A.S. Nagar, Punjab-160071 www.scl.gov.in



#### PRODUCT DESCRIPTION:

This LDO provides a fixed output voltage of 1.2V at 1.6A full load current. The LDO is stable with an external capacitor not lower than 10uF of ESR 1 ohm to 10 ohm. It supplies a nominal voltage of 1.2V to a circuit or load. The output voltage of the voltage regulator is regulated by the internal circuitry of the regulator to be relatively independent of the current drawn by the load, the supply or line voltage, and the ambient temperature. In order to protect voltage regulator from excessive temperatures or accidental short circuit, Over-temperature and Over-current protection circuit are included in this chip. Power good pin indicates whether output is within range of -5% of nominal output. The LDO can operate over a large temperature  $(T_A)$  range of -55°C to +100°C.

#### **FEATURES:**

- Nominal V<sub>OUT</sub>: 1.2V
- Maximum output current (FL): 1.6A
- Dropout Voltage: < 450mV at 1.6A load
- Initial voltage accuracy: 0% to +4%
- Voltage accuracy over load: <±1%
- Voltage accuracy over line: <±2%
- Quiescent current (I<sub>GND</sub>): <10mA at 1.6A load
- Lower (-5%) Power Good feature
- Over temperature shut down mechanism
- Short circuit current limiting feature
- Over current shutdown feature
- SCL 0.18µ CMOS technology

#### **APPLICATION:**

• Integrated solutions for analog and digital chips

#### **DEVICE SUMMARY:**

**Table 1: Device Summary** 

| DEVICE   | *DIE<br>SIZE     | PACKAGE | PINS         | DESCRIPTION      | TEMPERATURE<br>RANGE |
|----------|------------------|---------|--------------|------------------|----------------------|
| SC1028-0 | 5.6mm X<br>5.8mm | СОВ     | Minimum<br>7 | Evaluation Model | -55°C to +100°C      |

<sup>\*</sup>Die available for usage of the customer.



## **BLOCK DIAGRAM:**

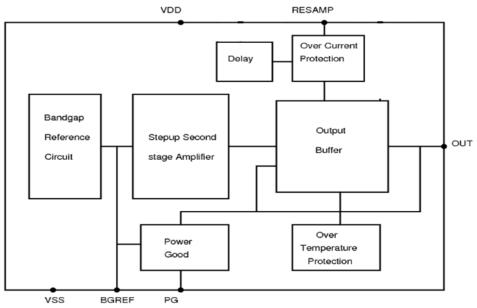


Figure 1: Block Diagram

## PIN CONFIGURATION (48 Pin COB):

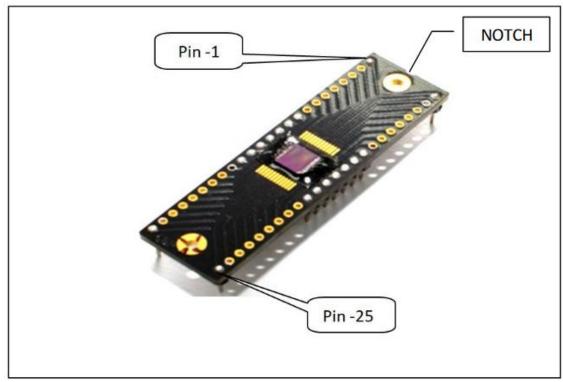


Figure 2: Pin Configuration



## PIN DESCRIPTION:

The pin description in accordance to the COB are provide in the below table.

**Table 2: COB Pin Details** 

| PIN NO | PIN NAME | PIN DESCRIPTION                                                                                     |
|--------|----------|-----------------------------------------------------------------------------------------------------|
| 9      | Vout     | Output sense (for 4-wire testing methodology)                                                       |
| 10     |          |                                                                                                     |
| 11     | Vout     | Output                                                                                              |
| 12     |          |                                                                                                     |
| 13     | BGR      | NC                                                                                                  |
| 14     | VSS      | Ground                                                                                              |
| 15     | PG       | Power Good out                                                                                      |
| 34     | Enable   | NC                                                                                                  |
| 35     | VSS      | Ground                                                                                              |
| 36     | Resamp   | Controls the short circuit current, 2.4kΩ resistance (for 3.6A SCC; 1.6A FL) b/w Resamp pin and VSS |
| 37     |          |                                                                                                     |
| 38     | Vin      | Input                                                                                               |
| 39     |          |                                                                                                     |
| 40     | Vin      | Input sense (for 4-wire testing methodology)                                                        |

<sup>\*</sup>SCC -> Short circuit current; FL -> Full Load

# **ABSOLUTE MAXIMUM RATING** (1):

Over operating free-air temperature range (unless otherwise noted),

**Table 3: Absolute Maximum Rating** 

| PARAMETER                           | CONDITIONS                                                                                                                            | MIN. | MAX. | UNIT |
|-------------------------------------|---------------------------------------------------------------------------------------------------------------------------------------|------|------|------|
| VDD                                 | w.r.t. AVSS                                                                                                                           | -0.3 | 4.3  | V    |
| Storage Temperature                 | Different package types may have different limitations on the storgae temperatures. customer needs to consult with the assembly house | -55  | 150  | °C   |
| ESD Tolerance (HBM)                 |                                                                                                                                       | 2000 |      | V    |
| Operating Ambient Temperature Range |                                                                                                                                       | -55  | +100 | °C   |

<sup>(1)</sup> Stresses beyond those listed under *absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.



## RECOMMENDED OPERATING CONDITIONS:

**Table 4: Recommended Operating Conditions** 

| SYMBOL                     | PARAMETER                 | MIN. | TYP. | MAX. | UNIT |
|----------------------------|---------------------------|------|------|------|------|
| $\mathbf{V}_{\mathbf{IN}}$ | Supply voltage            | 1.7  | 2.0  | 2.2  | V    |
| $I_{LOAD}$                 | Output drive current      | -    | -    | 1.6  | A    |
| $T_{\mathbf{A}}$           | Ambient temperature range | -55  | -    | +100 | °C   |

## DC ELECTRICAL SPECIFICATIONS:

**Test condition:** All Specifications:  $V_{IN}$  =1.7V-2.2V,  $C_{IN}$  = 1uF,  $C_{OUT}$  = 10µF, Resamp = 2.4k $\Omega$ ,

 $T_A = -55$ °C to +100°C. Full Load (FL) = 1.6A

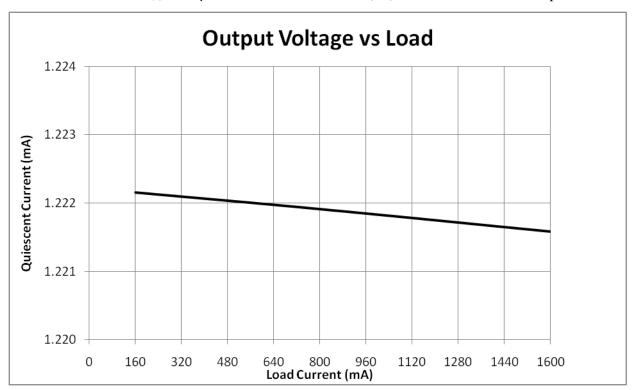
**Table 5: DC Electrical Specification** 

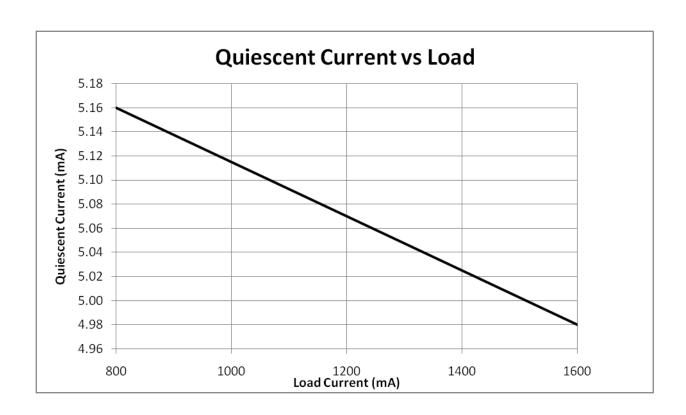
| PARAMETER                   | TEST CONDITIONS                                             |                            | SC1028-0 |       |       | UNITS  |
|-----------------------------|-------------------------------------------------------------|----------------------------|----------|-------|-------|--------|
| PARAVIETER                  |                                                             |                            | MIN      | TYP   | MAX   | UNIIS  |
| Nominal Voltage             | $I_{LOAD} = FL$                                             |                            | 1.20     | 1.22  | 1.23  | V      |
| Nominal voltage             | $Vin = 2.2V, I_{LOAD} = FL$                                 |                            | 1.21     | 1.22  | 1.24  |        |
| Initial Accuracy            | $I_{LOAD} = FL$                                             |                            | -        | -     | 3     | %      |
| Temperature<br>Coefficient  | $I_{LOAD} = FL$                                             |                            | 20       | 40    | 100   | ppm/°C |
| Load Regulation             | $10\%$ of FL $\leq I_{LOA}$                                 | $_{\rm D} \le 100\%$ of FL | -        | 0.25  | 0.95  | %      |
| Line Degulation             | $1.7V \leq V_{IN} \leq$                                     | $I_{LOAD} = 50\%$ of FL    | -        | 0.750 | 0.95  | %      |
| Line Regulation             | 2.2V,                                                       | $I_{LOAD} = 100\%$ of FL   | -        | 0.95  | 1.50  | - %    |
| Duon ant Valtage            | $I_{LOAD} = 50\%$ of FL                                     |                            | 0.150    | 0.175 | 0.220 | V      |
| Dropout Voltage             | $I_{LOAD} = 100\%$ of FL                                    |                            | 0.310    | 0.375 | 0.450 |        |
| Quiescent Current           | $I_{LOAD} = 50\%$ of FL                                     |                            | 4        | 4.5   | 6.5   | mA     |
| Quiescent Current           | $I_{LOAD} = 100\%$ of FL                                    |                            | 4        | 4.5   | 6.0   |        |
| Output Noise                | $I_{LOAD} = FL$ (100 kHz to 10 MHz)                         |                            | 70       | 75    | 80    | μVrms  |
| Temperature<br>Shutdown     | I <sub>LOAD</sub> = No Load                                 |                            | -        | 165   | -     | °C     |
| Power Good                  | $I_{LOAD} = Incremental$                                    |                            | -        | -5%   | -     | %      |
| Power Good<br>Response time | $I_{LOAD} = Incremental$                                    |                            | -        | 50    | -     | ms     |
| Short circuit current       | $I_{LOAD} = Short circuit current$<br>$Resamp = 2.4k\Omega$ |                            | 3.2      | 3.4   | 3.8   | A      |
| Over Current shutdown delay | I <sub>LOAD</sub> = Short circuit current                   |                            | 1        | 1.5   | 2     | S      |



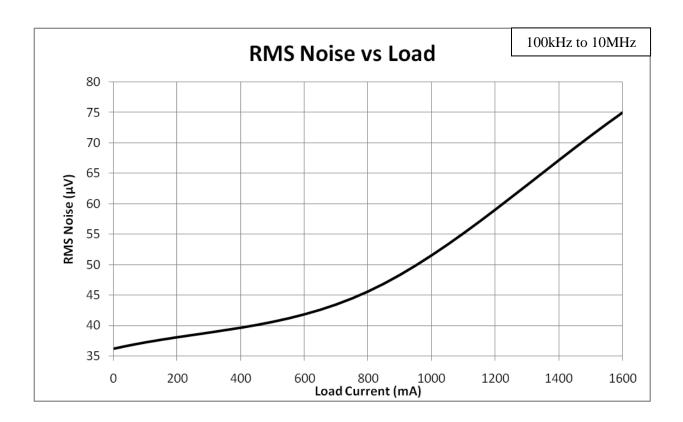
#### **TYPICAL CHARACTERISTICS:**

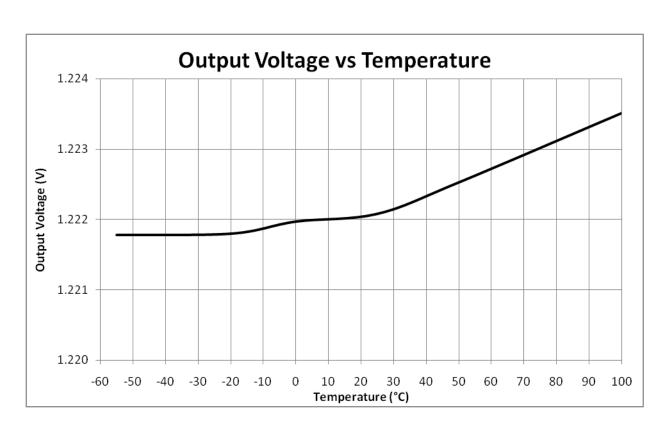
 $V_{IN}$  =1.7V,  $C_{IN}$  = 1uF,  $C_{OUT}$  = 10µF,  $T_A$  = 23±2°C, Full Load (FL) = 1.6A unless otherwise specified.



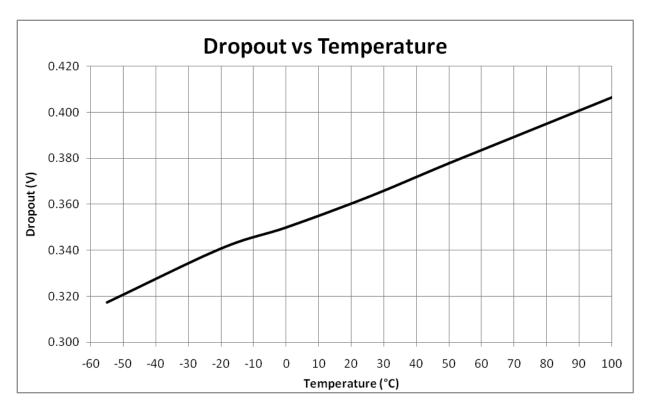


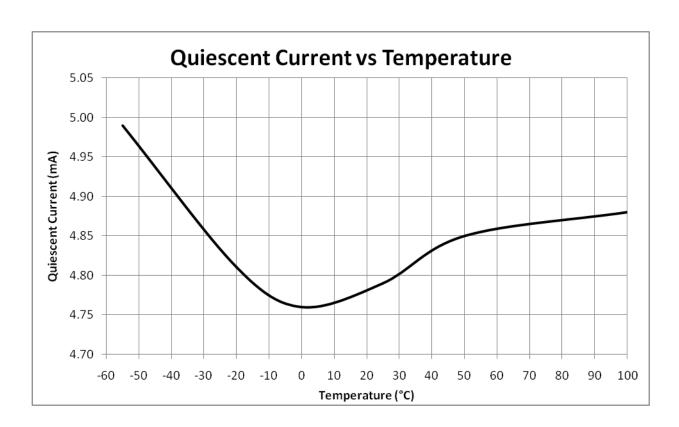














#### **KEY TERMS:**

#### i) Load Regulation

It measures the ability of the regulator to maintain the specified output voltage under different load conditions. It is specified as the percentage change in the output voltage relative to the nominal output voltage  $(V_{NOM})$ .

Load Regulation = 
$$\frac{\Delta Vout}{Vnom}$$
 100 %

#### ii) Line Regulation

It measures the ability of the regulator to maintain the specified output voltage over a range of input voltages. It is specified as percentage per Volt change in the output voltage as the input line voltage changes over its largest allowable range.

Line Regulation = 
$$\frac{\Delta Vout}{\Delta Vin}$$
 X  $\frac{100}{Vnom}$   $\frac{\%}{V}$ 

#### iii) Temperature Coefficient

It measures the ability of the regulator to maintain the specified output voltage over a range of temperature. It is specified as ppm per °C change in the input voltage over its full allowable temperature range.

$$TC = \frac{\Delta Vout}{\Delta T} \times \frac{10^6}{Vnom} \frac{ppm}{^{\circ}C}$$

#### iv) Dropout Voltage

It is the minimum voltage drop between input line voltage and output voltage, until the output voltage remains within 1% of its nominal value.

## v) Bias current or Quiescent Current Test

It is the total bias current (different from load current) consumed by different blocks of voltage regulator for their operation.

\*\*\*\*\*\*\*\*

## **IMPORTANT NOTICE**

Semi Conductor Laboratory (SCL) reserves the right to make corrections, enhancements, improvements and other changes to its semiconductor products and specifications, and to discontinue any product. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete.

Reproduction of significant portions of SCL information in SCL data sheets is permissible only if reproduction is without alteration and is accompanied by all associated conditions, limitations, and notices. SCL is not responsible or liable for such altered documentation.